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DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L17	6628671.pn. or 6751737.pn.	4	L17
L16	L15 and (sleep$5 or sav$3)	77	L16
L15	(portable or handheld or batter$3 or cellular or moble or wireless or mobile) near100 context$3 near3 switch$5	276	L15
L14	L13 not l9	104	L14
L13	(portable or handheld or batter$3 or cellular or moble or wireless) near100 context$3 near3 switch$5	170	L13
L12	l9 not l6	30	L12
L11	L9 not l6	30	L11
L10	L9 not l8	42	L10
L9	(portable or handheld or batter$3 or cellular) near100 context$3 near3 switch$5	66	L9
L8	batter$3 near200 context$3 near3 switch$5	25	L8
L7	L6 not l5	12	L7
L6	(portable or handheld or batter$3) near100 context$3 near3 switch$5	36	L6

L5	batter$3 near100 context$3 near3 switch$5	25	L5
L4	batter$3 near50 context$3 near3 switch$5	25	L4
L3	batter$3 near25 context$3 near3 switch$5	23	L3
L2	batter$3 and context$3 near3 switch$5	562	L2
L1	batter$3 near8 context$3 near3 switch$5	10	L1

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Display Format: Citation Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
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IEEE STD	IEEE Standard

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1. **An efficient semaphore implementation scheme for small-memory embedded systems**
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